





IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Steven K. Hsu et al.

Title:

VOLTAGE-LEVEL CONVERTER

Docket No.:

Examiner:

884.453US1

Filed:

June 29, 2001

Anh Q. Tran

Serial No.: 09/896,523

Due Date: December 10, 2002

IC 2800 MAIL ROOM

Group Art Unit: 2819

BOX AF

Commissioner for Patents Washington, D.C. 20231

We are transmitting herewith the following attached items (as indicated with an "X"):

X A return postcard.X An Amendment arX Clean Version of I

X An Amendment and Response Under 37 CFR 1.116 (3 Pages).

X Clean Version of Pending Claims (2 Pages).

Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional required fees or credit overpayment to Deposit Account No. 19-0743.

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Reg. No. 35,635

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: BOX AF, Commissioner for Patents, Washington, D.C. 20231, on this /Z day of November, 2002.

Danny J. Padys

Name

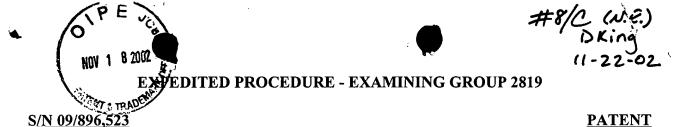
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(GENERAL)



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AMENDMENT & RESPONSE UNDER 37 C.F.R. § 1.116

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In response to the final office action mailed September 10, 2002, please amend Be application as follows.

IN THE CLAIMS

Please substitute the claim set in the appendix entitled Clean Version of Pending Claims for the previously pending claim set. The substitute claim set is intended to reflect amendment of previously pending claims 1 and 32. The specific amendments to individual claims are detailed in the following marked up set of claims.

Please amend claims 1 and 32.

- (Twice Amended) A voltage-level converter comprising: 1.
- a static voltage-level converter including an inverter coupled to no more than two transistors in the static voltage-level converter each of the no more than two transistors directly coupled to a voltage level; and
 - a split-level output circuit coupled to the static voltage-level converter.
- 32. (Amended) The voltage level converter of claim [32] 2, wherein the second transistor of the first pair of transistors and the second transistor of the second pair of transistors are insulated gate field-effect transistors.